

## Senior Analog/Mixed-Signal IC Design Engineer

HONG KONG OR SHENZHEN, FULL-TIME REGULAR

Senior Analog/Mixed-Signal Design Engineer works on high-performance analog-to-digital converter (ADC) and digital-to-analog converter (DAC). The successful candidate in this role will do behavioral modeling from specifications, transistor-level circuit design, layout design and supervision, silicon evaluation, and characterization to final product until mass production.

### Qualifications

- Must have a track record of mass production from designs
- MS with 6+ years of work experience in high-performance analog/mixed-signal/RF IC design and development in deep submicron CMOS processes
- Experience in 28nm HPC, 16nm FFC/FF+ or below will be highly preferable
- Must be fully familiar with high-resolution, high-speed ADC and DAC data converter design knowledge and techniques
- Strong understanding noise and mismatch issues in transistor-level circuit design
- Experience in designing high-performance building blocks such as precision bandgap reference and buffer, linear regulators, amplifiers, comparators, oscillators, and other complex mixed-signal architectures such as PLL, DLL is highly desirable
- Must have experience in silicon evaluation on laboratory, and familiar with the use of standard test equipment
- Experience with analog and digital behavioral modeling in MATLAB, and synthesis of digital logic control blocks
- Must be familiar with Cadence design environment, mixed-signal circuit simulation tools, layout verification and parasitic extraction tools
- Must be familiar with design techniques for aging, electromigration, ESD, substrate noise in deep submicron CMOS process
- Must be able to work independently according to schedules and be team player
- Strong written and verbal communication skills

Contact: [lok@caelustech.com](mailto:lok@caelustech.com) and [morrishe@caelustech.com](mailto:morrishe@caelustech.com)