



Job Title: Analog Design Engineer

Duties

- Participate in a team to design and layout of high-speed ICs for optical fiber/wireline communications, Design, verification, simulation and optimization for amplifier, ADC/DAC, bandgap, analog buffer, LDO, charge pump, voltage reference, PLL, oscillator, SerDes, etc. Create support documents ,

Requirements

- Bachelor degree or above in Electronic Engineering.
- Knowledge in analog and mixed-signal circuit design.
- Familiar with simulation tools such as SPICE, SPECTRE
- Experienced in high-speed circuit design is a plus Good programming in Perl, TCL and Shell programming is preferred.
- Knowledge in CMOS IC physical layout design and able to use various EDA tools.
- Good understanding of semiconductor theory and structures of devices.