

Digital Design Engineer

Job Requirements:

- Bachelor Degree or above in Electronic Engineering, Computer Engineering or similar discipline
- Knowledge in ASIC/FPGA design methodology; able to code in 'C' and Verilog RTL
- Proficient in Verilog, Universal Verification Methodology (UVM) and debugging is required
- Knowledge of analog circuit design and analysis (Op-Amp, references, pll, adc, dac);
- Hands-on IC layout skills is preferable
- Related working experience in semi-conductor industry is preferred, fresh graduates are welcomed
- Self-motivated, well-organized, independent and excellent interpersonal and communication skills
- Excellent troubleshooting and problem solving skills with smart common sense
- Fluent in spoken and written English, Mandarin and Cantonese