

Allystar Technology Co., Ltd.

*Units 701-702, 7/F, Lakeside 1,
No. 8 Science Park West Avenue,
Hong Kong Science Park,
Sha Tin, Hong Kong*

Contact Information

E-mail: Sue.yu@allystar.com

Allystar Technology specializes in Location Navigation Algorithm Development. Our vision is to redefine the GNSS/GPS market with our advanced technology in Global Position Satellite receiver and navigation applications.

Opening Job Position (Hong Kong)

Senior Physical Design Engineer

Job Responsibilities:

1. Work on physical design and integration of IP into SoC
2. Floor plan based on area, power and timing constraints
3. Perform time-driven Place and Route and Clock tree synthesis
4. Perform timing closure and implement fix to meet timing and power budget
5. Full chip physical verification (DRC/LVS/EM/IR) and implement fixes to meet requirements
6. Generate GDS and data stream out

Job requirements:

1. 5+ years of hands on experience in physical design
2. Hands-on experience of Cadence's EDA tools for physical design implementation in advanced technology
3. Experience in hierarchical design, budgeting, multiple voltage domains and multiple clock domains
4. Strong expertise in Physical Verification to debug LVS/DRC issues at block level
5. Solid understanding of STA and timing constraints
6. Experience in managing foundry process / design kit is an advantage
7. Knowledge in ESD / Latchup is an advantage