

## ***Allystar Technology Co., Ltd.***

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### Contact Information

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*Allystar Technology specializes in Location Navigation Algorithm Development. Our vision is to redefine the GNSS/GPS market with our advanced technology in Global Position Satellite receiver and navigation applications.*

## **Opening Job Position (Hong Kong)**

### **Digital IC Design Engineer**

#### **Job Responsibilities:**

Candidate will participate in all phases of VLSI development from concept and architecture to design implementation, verification, test plan, chip testing and debugging. Major activities are

1. RTL coding, design verification, logic synthesis, DFT and static timing analysis and etc
2. Develop verification environment to improve test coverage
3. Support tape-out and post-silicon activities including validation, characterization, and production test
4. Research latest DSP algorithm and its modelling and implementation
5. Prepare technical documents

#### **Job Requirements:**

1. Experience in one or more of the following areas:
2. Verilog based logic design and synthesis, constrained random testbench with System Verilog & UVM, assertion based design verification
3. Knowledge of low power logic design
4. Knowledge of digital signal processing algorithm and its implementation
5. Familiar with Unix/Linux environment and scripting languages such as C shell, Perl, TCL is a plus
6. Experience with DFT, BIST, SCAN insertion, ATPG is advantage

We offer competitive salary packages including 13th month salary and performance bonus. Fresh graduate are welcome. More experience candidate will be considered as Senior Digital IC Design Engineer.